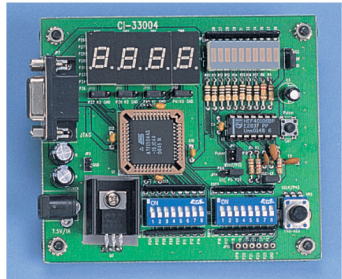


## CI-33004 Experiment Board

1. Atmel ATF1504-15 FPGA chip (compatible Altera MAX 7064) contains 64 Microcells. (over 1000 usable gates) re-programming over 10k times.
2. Users could use graphic editor and text editor (HDL) to designing and simulating digital circuit. Via the developing software of Altera MAX+plus II.
3. Downloads configuration data from PC to FPGA Via RS-232 cable.
4. Without the shortcomings of time-consuming, low efficiency and high cost while studying the traditional digit/logic design, users could easily learn the hardware and software design of FPGA form SN-AT1504.
5. No need of insulated solid wire, any complicated and fast circuit design, simulations and experiments could be finished at short time. The software is with the function of simulations.
6. Using JTAG writing.



## Specification

1. 16 DIP switches for digital state input.
2. 10-BAR LEDs for output state display.
3. 4-digit 7-segment display for static and dynamic driving operation.
4. 2 channels clock pulse output.  
Adjustable frequency range :  
10Hz to 350Hz ( $\pm 20\%$ )  
Continuously frequency range :  
up to 3.5kHz ( $\pm 20\%$ )
5. Altera MAX+plus II V9.23.
6. Altera translate software POF2JED and ISP writing software.
7. For Win95/98

## List of experiment

1. Designing combinational logic circuits.
2. Designing sequential logic circuit.
3. Designing flip-flops circuit.
4. Designing counters and applications.
5. Designing ALUs and applications.
6. Encoder/decoder and multiplexer/demultiplexer experiment.
7. Production and application of digital subjects.

## Accessories

1. BYTE blaster
2. RS-232 cable
3. Adapter
4. User manual



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